

## **REMARKS**

Claims 1-48 are pending in the application of which claims 13-24 and 37-48 are withdrawn from consideration. Claims 1-12 and 25-36 are rejected. Claims 1, 8, 11, 12, 25, 26, 29, 30, 32, and 35 have been amended.

Applicant elects to prosecute the invention of group I, claims 1-12 and 25-36.

Claims 1-12 and 25-36 were objected to because of informalities. The claims have been amended to correct the informalities.

Claims 1-8, and 25-32 were rejected under 35 USC 102(b) as being anticipated by Itazu. Claims 9-12 and 33-36 were rejected under 35 USC 103(b) as being unpatentable over Itazu in view of Djaja. Claims 1 and 25 include "...generating a model of said module by replacing each of said plurality of transistors in said topology by a corresponding one of a plurality of current sources ...". The references of record do not show, teach, or suggest the above recited limitations of claims 1 and 25. The Itazu reference does not disclose replacing each transistor with a current source. Claims 2-12 depend from claim 1. Claims 26-36 depend from claim 25. Therefore, claims 1-12 and 25-36 are believed to be allowable over the references of record.

It is believed that the above remarks and amendments are fully responsive to the Official Action. Reconsideration and allowance are therefore respectfully requested.

Respectfully submitted,

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